Technical Steering Committee Update

*Jeff Scheel, TSC Chair*

IBM Distinguished Engineer, Cognitive Software Strategy
Introduction

Jeff Scheel, IBM, Distinguished Engineer

- 26 years with IBM
- 18 years leading IBM’s Linux on Power effort
- Chartered the OpenPOWER System Software Work Group and have served as Chair since inception (April 2014)
- Active participant in TSC activities, including documentation framework and tooling
Role of the TSC in an open ecosystem

- **Open Development**
  - Open software, open hardware

- **Collaboration of thought leaders**
  - Simultaneous innovation, multiple disciplines

- **Performance of POWER architecture**
  - Amplified capability

**WorkGroups**

- **Open Specifications**
  - Available for use
  - Enhanced by members

- **Encourage Innovation**
  - Reference Designs
  - Design guides and IO enablement

- **Engage community**
  - What is missing or incomplete?

It was true in 2014, and it remains today...

...the TSC is the team that implements the Board’s vision for the members, through the working groups.
Technical accomplishments in 2017

Completed Work Group Specifications
- Memory WG: OpenPOWER Memory Bus V1.0
- Accelerator WG: PSL/AFU Interface CAPI 2.0 v1.0
- System SW WG: 64bit ELF V2 ABI
- FSI WG: Open FSI Specification v1.0.0
- Compliance WG: OpenPOWER ISA Compliance Definition
- Compliance WG: OpenPOWER CAPI 1.0 Accelerator Compliance Test Specification
- Compliance WG: ELF V2 ABI Compliance TH/Ts Specification

Completed Work Group Notes
- Personalized Medicine WG: Porting and Benchmarking of BWAKIT pipeline on Open POWER architecture
- WG for Physical Science: Collecting Use Case Information: Method and Template for Physical Science

Completed Public Review
- 25GIO WG: Advanced Accelerator Adapter Electro-mechanical Specification

Public Review
- Compliance WG: OpenPOWER CAPI 2.0 Accelerator Compliance
- Compliance WG: OpenPOWER FSI Protocol Compliance Test Harness and Test Suite (TH/Ts) Specification
- OpenPOWER Ready WG: OpenPOWER Ready Definition and Criteria V2.0

Work In Progress
- Accelerator WG: CAPI-SNAP and CAPI Accelerator Reference Card
- System SW WG: 3 WG Notes and 5 specifications (LoPAR)
- Memory WG: OPBM V2.0 Spec (OpenCAPI attach – V1 was DMI attach)
- OpenPOWER Ready WG: Shift to driving participation
- Compliance WG: Additional compliance specs as appropriate
- Machine Learning WG: 1ST WG Note: Use Cases and Methods in creation
- Physical Sciences WG: 1ST WG Note: Collecting Use Case Information – in draft form
- IO WG: 2 WG Notes in Draft form
- FSI WG: Potential use of FSI on BMC
- HW Arch WG: Need to restart (CAIA V2, IODA3, OpenPOWER Profile V2)!
- Personalized Medicine WG: working on another WG Note describing accelerated pipeline
- Integrated Solutions WG: Need to refocus (discussing “workshop” with System Integrators)
- 25GIO WG: This WG will likely close once spec is approved (32G => OpenCAPI.org)

Footnotes:
1. Specification has been published
2. Specification has completed
Draft Goals for 2018

“Excitement and Participation”

Aspirational:
- Develop and drive a “Moonshot” around Ecosystem Developers – both HW and SW
- Encourage use/implementation of WG products

Operational:
- Each WG to identify one project that will excite and engage the WG membership
- TSC to identify and work with WG to drive Specifications into OPF Standards
  - Enables marketing splash about it being implemented
- Propose incentives for work group participation