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<b>Product / Component Designator</b>	TN71-BP012
<b>Short Product / Component Description</b>	2U single-socket OpenPOWER platform supports up to 1,024GB memory
<b>Tag string describing keywords, attributes, and tags for enhancing search results</b>	OpenPOWER, In-memory computing
<b>Product / Component Information URL</b>	<a href="http://www.tyan.com/Barebones_TN71-BP012_BSP012T71V14HR-4T-3">http://www.tyan.com/Barebones_TN71-BP012_BSP012T71V14HR-4T-3</a> <a href="http://www.tyan.com/Barebones_TN71-BP012_BSP012T71V14HR-4T-4">http://www.tyan.com/Barebones_TN71-BP012_BSP012T71V14HR-4T-4</a> <a href="http://www.tyan.com/Barebones_TN71-BP012_BSP012T71V14HR-4T-5">http://www.tyan.com/Barebones_TN71-BP012_BSP012T71V14HR-4T-5</a>
<b>Version of this OpenPOWER Ready Document used for the criteria</b>	Rev. 0.11
<b>Product / Component Category (System, IO, CAPI, Operating System, Application, System Support Component)</b>	System
<b>Ready Checklist</b>	
OpenPOWER ISA Profile processor Chipset	Yes. (POWER8™, CP1, and Centaur memory buffer with memory)
Firmware to initialize the hardware is a modest derivative of the OpenPOWER Abstraction Layer (OPAL) FirmWare base, with no firmware API/ABI changes that have not been accepted upstream.	Yes
At least one boot device. Storage or PXE Network boot.	Yes. Both storage & PXE boot are supported
Must be able to install and run a current LTS (Long Term Support) version of Ubuntu® Server.	Ubuntu 14.04 LTS is validated
System must successfully run an exerciser application.	Yes
PCIe® bus should support PCIe gen 1.0, 2.0 and 3.0 adapters.	Yes
A boot management chip to manage the Power On Reset (POR) and initial Initial Program Load (IPL) is needed	ASPEED AST2400 Baseboard Management Controller
A system level XML file as input to the OpenPOWER Build process for host firmware.	Yes
PNOR, VPD, Thermal and Power devices.	Yes